



Bhattacharya 3-5-8-1-25

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): D. Bhattacharya et al.

Case: 3-5-8-1-25

Serial No.: 10/768,771

Filing Date: January 30, 2004

Group: 2832

Examiner: Karl D. Easthom

Title: Improved Semiconductor Resistor

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria VA 22313-1450.

Signature: _____ Date: August 29, 2005

AMENDMENT AFTER FINAL REJECTION UNDER 37 C.F.R. §1.116

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In response to the final Office Action dated May 27, 2005, Applicants submit herewith the following Amendment for entry in the above-identified application:

*Entered
w/ R&E
dated
9-28-05*

*G. Stanley
10-6-05*

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